

REMARKS


Claims 1-26 are pending. Claims 1, 6 and 12 are amended to more particularly point out and distinctly claim Applicant's invention.

The Examiner repeated his rejection of Claims 1-12 under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent 5,841,396 ("Krasner"). In response to Applicants' arguments that Krasner failed to disclose or suggest a phase-locked loop to provide a reference signal to a downconverter in a GPS device, the Examiner argues that Krasner teaches an automatic frequency control (AFC) circuit to provide a reference signal and that the AFC circuit may be replaced by a phase-locked loop. However, as amended, Claims 1, 6 and 12 each further recite that the phase-locked loop includes a "phase interpolated fractional frequency synthesizer", which provides the high precision in the reference signal (see, e.g., Applicants' Specification, at page 10, line 22 to page 12, line 26). Krasner does not disclose or suggest such a circuit. Accordingly, Applicants submit that Claims 1, 6 and 12 and their respective dependent Claims 2-5 and 7-11 are allowable over Krasner. Reconsideration and allowance of Claims 1-12 are therefore requested.

The Examiner rejected Claims 13-26 under the judicially created doctrine of double-patenting over Claims 1-14 of U.S. Patent 6,650,879. Applicant submits herewith a terminal disclaimer to overcome the Examiner's rejection.

For the above reasons, Applicant submits that all pending claims (i.e., Claims 1-26) are allowable. If the Examiner has any questions regarding the above, the Examiner is respectfully requested to telephone the undersigned Attorney for Applicant at 408-392-9250.

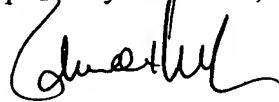
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Date of Signature

Respectfully submitted,



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